

CCC SW	Switch Type	1	2	3	4	5	6	7	8	Total Delay (ps)	Signal	To Board(s)
1	CDELAY	0	0	0	0	0	0	0	0	0	BX0	JSC
2	CDELAY	0	1	0	1	0	1	1	0	1505	ACPT (120)	JSC
3	VME	1	1	1	0	0	0	0	0	n/a	VECTOR	
8	DELAY	0	0	0	0	0	0	0	0	0	160 CLK	RC 6
9	DELAY	0	0	1	0	0	0	0	0	560	160 CLK	RC 4 & 5
10	DELAY	0	1	0	0	0	0	0	0	1120	BX0	RC 2 & 3
11	DELAY	0	1	0	1	1	1	0	0	1610	BX0	RC 0 & 1
12	DELAY	0	0	0	0	1	0	1	0	175	160 CLK	EIC 6
13	DELAY	0	0	0	1	0	1	0	0	350	160 CLK	EIC 4 & 5
14	OSC	0	0	0	0	0	0	0	0	0	OSC DELAY	CCC
15	DELAY	0	0	0	0	0	0	0	0	0	BX0	EIC 2 & 3
16	DELAY	0	0	0	0	0	0	0	0	0	BX0	EIC 0 & 1
17	OFFSET	0	0	0	0	0	0	0	0	0	BX0	EIC 0 to 6
18	DELAY	0	0	0	0	0	0	0	0	0	RESET	RC 6
19	DELAY	0	0	0	1	0	1	0	0	350	RESET	RC 4 & 5
20	DELAY	0	0	1	0	1	1	0	0	770	ACPT (120)	RC 2 & 3
21	DELAY	0	1	0	0	0	0	0	0	1120	ACPT (120)	RC 0 & 1
22	OSC	0	0	0	0	0	0	0	0	n/a	OSC DELAY	CCC
23	OSC	0	0	0	0	0	0	0	0	0	OSC DELAY	CCC
24	OSC	0	0	0	0	1	0	0	0	140	OSC DELAY	CCC
25	DELAY	0	0	0	1	1	0	1	0	455	RESET	EIC 6
26	DELAY	0	0	1	1	0	0	0	0	840	RESET	EIC 4 & 5
27	PHASE	0	1	0	1	0	0	0	0	1400	SET_PHSE	RC & JSC
28	DELAY	0	0	0	0	0	0	0	0	0	ACPT (120)	EIC 2 & 3
29	DELAY	0	0	0	0	0	0	0	0	0	ACPT (120)	EIC 0 & 1
30	OSC	1	1	1	1	0	0	0	0	30	OSC DELAY	CCC
31	OFFSET	0	0	1	1	0	0	0	0	840	160 CLK	EIC 0 to 6