### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log ###

### Location of log file

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### RC Test 5ab - Backplane data paths ###
### Test run on 2004-08-26_18:00:03 ###
### HOST computer is: daffodil ###
### Run in vmedia kumac: rc_backplane_path_slot3.txt ###

Please fill in the backplane data paths CHECKLIST.

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!!!! RC to be tested has to be in slot 3 !!!!

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

Enter command (help for usage)>

SBS successfully booted and talked to the cards in the crate

The CCC, 1 EIC in slot 2, 6 RCs and the RC to be tested in slot 3 should be plugged in

Zero memories first.
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

RCTCrate::initialize() : vmeReset() successful
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 15000000

rctCrateTest: initialize() succeeded
Cards in the crate are: 54ba

RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Loading RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Verifying RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Loading EIC (b100, 15000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (b100, 15000000)

rctCrateTest: All tests successful

Now start vmedia script rc_backplane_path_slot3

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*** this is vmedia script rc_backplane_path_slot3.txt ***

---

for this test, the rc has to be in slot 3

Continue <return> ? Exit <Ctrl-D> ?

this eic has barcode
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
Compare this number with the RC bar code

for this test, the crate has to be loaded with all seven rc’s Important

These values should be read back

Initial setup done.

Put a 68-pin cable from rc 2 j4 to rc 3 j4 Follow these directions, no need to first power down

Repeat:

Check the signals as specified in the checklist - when done type <return> for next signal

Check 4 bits on U125 and 3 bits on U126;
pattern 7F should result in 111 1111, i.e. a “1” on each of the 7 pins;
pattern 00 should result in 000 0000, i.e. a “0” on each of the 7 pins;
double pulse means seeing 1010 on EACH of the 8 pins

Hit return to zero the memory and exit

Bye

RC Test 5b End