### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log ###

***************************************************************
RC Test 5c - Backplane data paths ****************************
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Test run on  2004-08-26_18:06:21
HOST computer is:  daffodil
Run in vmedia kumac: rc_backplane_path_slot0.txt
Please fill in the backplane data paths CHECKLIST.
***************************************************************

!!!! RC to be tested has to be in slot 0 !!! Important !
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
Enter command (help for usage)>
RCT boot succeeded with 9 cards.
Zero memories first.
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
RCTCrate::initialize() : vmeReset() successful
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 15000000
rctCrateTest: initialize() succeeded
Cards in the crate are: 54ba
RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Loading RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Verifying RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Loading EIC (b100, 15000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (b100, 15000000)
rctCrateTest: All tests successful
Now start vmedia script rc_backplane_path_slot0

***************************************************************
*** this is vmedia script rc_backplane_path_slot0.txt ***
***************************************************************
for this test, the rc has to be in slot 0 Important
Continue <return> ? Exit <Ctrl-D> ? type <return> here
this rc has barcode
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
for this test, the crate has to be loaded with all seven rc’s **important**

- `12000006 -> F565`
- `14000006 -> FA05`
- `16000006 -> 0060`
- `18000006 -> F600`
- `1B000006 -> F760`
- `1D000006 -> FE00`
- `1F000006 -> F820`
- `12000000 -> 0202`
- `14000000 -> 0202`
- `16000000 -> 0202`
- `18000000 -> 0202`
- `1B000000 -> 0202`
- `1D000000 -> 0202`
- `1F000000 -> 0202`

initial setup done.

west, should see 7f. -- next ?

west, should see 00. -- next ?

west, should see double pulse. hit return to zero the memory and exit.

VMEDia> exit type 'exit' here

Bye

Check the signals as specified in the checklist - when done type <return> for next signal

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Check 4 bits on U125 and 3 bits on U126; pattern 7F should result in 111 1111, i.e. a “1” on each of the 7 pins; pattern 00 should result in 000 0000, i.e. a “0” on each of the 7 pins; double pulse means seeing 1010 on EACH of the 8 pins