!!! RC to be tested has to be in slot 5 !!!! IMPORTANT!

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

Enter command (help for usage)>

RCT boot succeeded with 9 cards.

Zero memories first.

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

rctCrateTest::initialize() : vmeReset() successful

rctCrateTest::initialize() : Defined RCTClockControlCard 10000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 12000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 14000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 16000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 18000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 1b000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 1d000000

rctCrateTest::initialize() : Defined RCTReceiverCard with address 1f000000

rctCrateTest::initialize() : Defined RCTElectronIsolationCard with address 15000000

rctCrateTest: initialize() succeeded

Cards in the crate are: 54ba

rctCrateTest: All tests successful

Now start vmedia script rc_backplane_path_slot5

****************************************************
*** this is vmedia script rc_backplane_path_slot5.txt ***
****************************************************

for this test, the rc has to be in slot 5 Important

this rc has barcode

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
for this test, the crate has to be loaded with all seven rc’s

 initial setup done.

 put a 68-pin cable from rc 4 j4 to rc 5 j4

 Follow these directions, no need to first power down

 Should see a “1” on U128, pins 24, 28, 4, 6

 Should see a “1” on U128, pins 23, 27, 3, 5

 type <return> here

 VMEDia> exit type 'exit' here

 Bye

######################################################################## RC Test 5e End ##################################################################