### Log file for this test: /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log ###

##### Location of log file

******************************************************************************
# RC Test 5f - Backplane data paths #
### Test run on 2004-08-26_18:21:48
### HOST computer is: daffodil
### Run in vmedia kumac: rc_backplane_path_slot1.txt

Please fill in the backplane data paths CHECKLIST.

******************************************************************************

!!!! RC to be tested has to be in slot 1 !!!!

**Important!**

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

Enter command (help for usage)>

**SBS successfully booted and talked to the cards in the crate**

**RCT boot succeeded with 9 cards.**

Enter command (help for usage)>

Zero memories first.

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

RCTCrate::initialize() : vmeReset() successful
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 15000000

**rctCrateTest: initialize() succeeded**

Cards in the crate are: 54ba

RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Loading RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Verifying RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Loading EIC (b100, 15000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (b100, 15000000)

**rctCrateTest: All tests successful**

Now start vmedia script rc_backplane_path_slot1

*****************************************************************
** this is vmedia script rc_backplane_path_slot1.txt **
*****************************************************************

for this test, the rc has to be in slot 1 **Important**

Continue <return> ? Exit <Ctrl-D> ?

this rc has barcode

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

Check that the verification doesn't fail. When it does, leave the script (Ctrl-D) and vmedia (exit) and redo >run_RC_test5f

Important type <return> here
initial setup done.
start with paths for corner sharing.
put a 68-pin cable from rc 0 j4 to rc 1 j4 Follow these directions, no need to first power down
Continue <return> ? Exit <Ctrl-D> ? Should see a "1" on U66, pins 24, 28, 4, 6
sw corner -- next ? Should see a "1" on U66, pins 23, 27, 3, 5
Continue <return> ? Exit <Ctrl-D> ?

These values should be read back

Continue <return> ? Exit <Ctrl-D> ?

Compare this number with the RC bar code

important

Type 'exit' here

Bye

############################ RC Test 5f End #################################