### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log ###

Location of log file

RC Test 6a - Data sharing via cables

Test run on 2004-08-26_18:43:29

HOST computer is:  daffodil

Run in vmedia kumac: check_j5.txt

Please fill in the data sharing CHECKLIST.

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sstream points to slot 5 !!! Important!

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

Enter command (help for usage)>

RCT boot succeeded with 9 cards.

Zero memories first.

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64

RCTCrate::initialize() : vmeReset() successful

RCTCrate::initialize() : Defined RCTClockControlCard 10000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000

RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000

RCTCrate::initialize() : Defined RTElectronIsolationCard with address 13000000

rctCrateTest: initialize() succeeded

Cards in the crate are: 54ae

RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)

RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)

RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)

RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)

RCTCrate::doZeroPatternTest() : Loading RC (0, 16000000)

RCTCrate::doZeroPatternTest() : Verifying RC (0, 16000000)

RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)

RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)

RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)

RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)

RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)

RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)

RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)

RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)

RCTCrate::doZeroPatternTest() : Loading EIC (f900, 13000000)

rctCrateTest: All tests successful

Now start vmedia script check_j5.txt

**************************************************************************

*** This is vmedia script check_j5.txt ****************************

for this test, the rc to be tested has to be either in slot 5 or in slot 1

rc in slot 1 has barcode
Device to open: /dev/btp64
14000006 -> FA00
rc in slot 5 has barcode
1D000006 -> FE00 Compare this number with the RC bar code
the eic has to be in slot 1 Important
Continue <return> ? Exit <Ctrl-D> ? type <return> here

for this test, the crate has to be loaded with all seven rc’s
12000006 -> F565
14000006 -> FA05
16000006 -> 0060
18000006 -> F600
1B000006 -> F760
1D000006 -> FE00
1F000006 -> F820
12000000 -> 0202
14000000 -> 0202
16000000 -> 0202
18000000 -> 0202
1B000000 -> 0202
1D000000 -> 0202
1F000000 -> 0202
plug in cable in j5 from card 1 to card 5. Follow these directions, no need to first power down
west, should see 7f. -- next ? Check the signals as specified in the checklist -
Continue <return> ? Exit <Ctrl-D> ? when done type <return> for next signal

west, should see 00. -- next ? Repeat:
Continue <return> ? Exit <Ctrl-D> ?

west, should see double pulse.
hit return to zero data.
Continue <return> ? Exit <Ctrl-D> ? type <return> here

type 'exit' here

VMEDia> exit type 'exit' here
Bye

Check 4 bits on U125 and 3 bits on U126; Check 4 bits on U125 and 3 bits on U126;
pattern 7F should result in 111 1111, i.e. a pattern 7F should result in 111 1111, i.e. a
“1” on each of the 7 pins;
“1” on each of the 7 pins;
pattern 00 should result in 000 0000, i.e. a pattern 00 should result in 000 0000, i.e. a
“0” on each of the 7 pins;
“0” on each of the 7 pins;
double pulse means seeing 1010 on EACH double pulse means seeing 1010 on EACH
of the 8 pins
of the 8 pins

These values should be read back

Important