### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log  ###

#################################################################
### Location of log file

#################################################################
### Location of log file

### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26.log  ###

### Test run on 2004-08-26_19:14:30
### HOST computer is:  daffodil
### Run executable output_electron
Please, check bit patterns on JSC Mezz J5, J6.

Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
Enter command (help for usage)>

RCT boot succeeded with 16 cards.

Cars in the crate are: 7fff

SBS successfully booted and talked to the cards in the crate

Crane has to be fully loaded, with RC under test in slot 3:

1 CCC, 1 JSC, 7 EICs, 6 RC and the RC under test

Zero memories first.

Enter command (help for usage)>

rctCrateTest: initialize() succeeded

Check that the verification doesn't fail.
When it does, leave the script (Ctrl-D) and vmedia (exit) and redo>

run_EIC_test7
RCTCrate::doZeroPatternTest() : Loading EIC (0, 1a000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 1a000000)
RCTCrate::doZeroPatternTest() : Loading EIC (0, 1c000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 1c000000)
RCTCrate::doZeroPatternTest() : Loading EIC (f900, 1e000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (f900, 1e000000)
RCTCrate::doZeroPatternTest() : Loading JSC (ff00, 19000000)
RCTCrate::doZeroPatternTest() : Verifying JSC (ff00, 19000000)

rctCrateTest: All tests successful
Now fill the appropriate patterns into the memory.
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
Card E0 is inserted
Card R0 is inserted
Card E1 is inserted
Card R1 is inserted
Card E2 is inserted
Card R2 is inserted
Card E3 is inserted
Card R3 is inserted
Card JS is inserted
Card E4 is inserted
Card R4 is inserted
Card E5 is inserted
Card R5 is inserted
Card E6 is inserted
Card R6 is inserted
A total of 15 cards inserted
Incrementing 0 memory for 11400000
Incrementing 0 memory for 13400000
Incrementing 0 memory for 15400000
Incrementing 0 memory for 17400000
Incrementing 0 memory for 1A400000
Incrementing 0 memory for 1C400000
Incrementing 0 memory for 1E400000
Programming Card 0
Programming Card 1
Programming Card 2
Programming Card 3
Programming Card 4
Programming Card 5
Programming Card 6

## You can now start checking the output pattern on JSC Mezz J5, J6.
############################ RC Test 8 End ##################################