CMS TRIGGER ASICs

Cal. Trigger	Technology	Area (mm2)	Nb of pins	Nb of units	Dev Cost	Unit/Cost	Institute	Dev Cost V8	Unit/Cost V8
TPG	CMOS rad tol.	130	200	2000	150	0.64	Palaiseau	100	0.3
Sync Tx	Rad tolerant			4000	130	0.3	Lisbon	100	0.2
Sync Rx	BiCMOS			1000	130	0.47	Wisc. (Lisbon)	100	0.35
Adder	GaAs .6 um			530	65	0.23	Wisconsin	65	0.23
Electron ID	GaAs .6 um			320	104	0.39	Wisconsin	65	0.23
Boundary Scan	CMOS			350	65	0.2	Wisconsin	0	0
Sorter	BiCMOS .8 um	60	150	100	76	0.08	Bristol	76	0.08
RPC Trigger									
PAC	0.5/ 0.35 um	80	128	30000		0.04	Warsaw		
Sorter	CMOS 0.7 um	100	204	2000	40	0.12	Bari		
ROC							Warsaw		
CSC Trigger									
LCT wire							USA		
LCT strip							USA		
BCI							USA		
DT Trigger									
BTI	ECAT 0.5 um	25	64	50000	100	0.01	Padova		
Track finder		10	300	420	500	0.2	Vienna		