

WBS Number	Description	EDIA (k\$)	M&S (k\$)	Mfg Labor (k\$)	Base Cost (k\$)	Cont (k\$)	Cont (%)	Total Cost (k\$)	DOE Request (k\$)	NSF Request (k\$)
3	Trigger and Data Acquisition	3,454	7,461		10,915	5,712	52	16,627	15,680	947
3.1	Trigger	2,185	3,953		6,137	3,130	51	9,267	9,267	

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3.1.1	CSC Muon Trigger	937	812		1,749	937	54	2,687	2,687	
3.1.1.1	Muon Port Cards (MPC)	215	292		507	253	50	760	760	
3.1.1.1.1	MPC Design	81			81	40	50	121	121	
3.1.1.1.1.1	MPC Initial System Design	20			20	10	50	30	30	
3.1.1.1.1.2	MPC Initial Prototype Design	43			43	21	50	64	64	
3.1.1.1.1.3	MPC Prototype Design	18			18	9	50	27	27	
3.1.1.1.2	MPC Proto. Construction	10	42		52	26	50	78	78	
3.1.1.1.2.1	MPC Proto. Constr. Manage	10			10	5	50	15	15	
3.1.1.1.2.2	MPC Proto. Cables		2		2	1	50	3	3	
3.1.1.1.2.3	MPC Proto. Components		12		12	6	50	18	18	
3.1.1.1.2.4	MPC Proto. Boards		8		8	4	50	12	12	
3.1.1.1.2.5	MPC Proto. Optical Links		10		10	5	50	15	15	
3.1.1.1.2.6	MPC Proto. TTC Links		10		10	5	50	15	15	
3.1.1.1.3	MPC Proto. Test	20			20	10	50	30	30	
3.1.1.1.4	MPC ASIC and Board Design	60			60	30	50	90	90	
3.1.1.1.5	MPC Active Components		143		143	71	50	214	214	
3.1.1.1.5.1	MPC FPGAs		58		58	29	50	86	86	
3.1.1.1.5.2	MPC JTAG controller		1		1	0	50	1	1	
3.1.1.1.5.3	MPCEPROM		2		2	1	50	3	3	
3.1.1.1.5.4	MPC Glinks to Track Finder		52		52	26	50	78	78	
3.1.1.1.5.5	MPC TTC optical links		14		14	7	50	22	22	
3.1.1.1.5.6	MPC Channel Links to TMBs		16		16	8	50	24	24	
3.1.1.1.6	MPC Boards	29	35		64	32	50	97	97	
3.1.1.1.6.1	MPC Board Prod. Manage	15			15	8	50	23	23	
3.1.1.1.6.2	MPC Board Setup		2		2	1	50	3	3	
3.1.1.1.6.3	MPC Board Fabrication		18		18	9	50	26	26	
3.1.1.1.6.4	MPC Board Assembly		7		7	4	50	11	11	
3.1.1.1.6.5	MPC Connectors-copper		3		3	2	50	5	5	
3.1.1.1.6.6	MPC Misc components		5		5	2	50	7	7	
3.1.1.1.6.7	MPC Inspection and Test	14			14	7	50	22	22	
3.1.1.1.7	MPC Mounting, Power, Cooling		14		14	7	50	22	22	
3.1.1.1.8	MPC Installation	15			15	8	50	23	23	
3.1.1.1.9	MPC Spares		26		26	13	50	39	39	
3.1.1.1.10	MPC Motherboard Cables		32		32	16	50	48	48	

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3.1.1.2	Sector Receivers (SR)	202	208		410	205	50	615	615	
3.1.1.2.1	SR Design	80			80	40	50	120	120	
3.1.1.2.1.1	SR Initial System Design	20			20	10	50	30	30	
3.1.1.2.1.2	SR Initial Proto. Design	30			30	15	50	45	45	
3.1.1.2.1.3	SR Proto. Design	30			30	15	50	45	45	
3.1.1.2.2	SR Proto. Construction	10	20		30	15	50	45	45	
3.1.1.2.2.1	SR Proto. Constr. Manage	10			10	5	50	15	15	
3.1.1.2.2.2	SR Proto. Components		12		12	6	50	18	18	
3.1.1.2.2.3	SR Proto. Boards		8		8	4	50	12	12	
3.1.1.2.3	SR Proto. Test	20			20	10	50	30	30	
3.1.1.2.4	SR ASIC and Board Design	53			53	26	50	79	79	
3.1.1.2.5	SR Active Components		124		124	62	50	186	186	
3.1.1.2.5.1	SR FPGA's		53		53	27	50	80	80	
3.1.1.2.5.2	SR Glink Receivers		46		46	23	50	69	69	
3.1.1.2.5.3	SREPROMs		2		2	1	50	3	3	
3.1.1.2.5.4	SR FIFOs for DAQ		11		11	5	50	16	16	
3.1.1.2.5.5	SR Buffers		7		7	3	50	10	10	
3.1.1.2.5.6	SR Memory Lookups		5		5	3	50	8	8	
3.1.1.2.6	SR Boards	29	37		66	33	50	100	100	
3.1.1.2.6.1	SR Const. Manage	15			15	8	50	23	23	
3.1.1.2.6.2	SR Board Setup		2		2	1	50	3	3	
3.1.1.2.6.3	SR Board Fabrication		14		14	7	50	22	22	
3.1.1.2.6.4	SR Board Assembly		7		7	4	50	11	11	
3.1.1.2.6.5	SR Connectors-copper		1		1	1	50	2	2	
3.1.1.2.6.6	SR Misc components		5		5	2	50	7	7	
3.1.1.2.6.7	SR Front Panels and Hardware		7		7	4	50	11	11	
3.1.1.2.6.7	SR Inspection and test	14			14	7	50	22	22	
3.1.1.2.7	SR Installation	10			10	5	50	15	15	
3.1.1.2.8	SR Spares		27		27	13	50	40	40	
3.1.1.3	CSC Sector Processors (SP-CSC)	170	76		246	160	65	406	406	
3.1.1.3.1	SP-CSC Design	60			60	39	65	99	99	
3.1.1.3.1.1	SP-CSC Initial System Design	10			10	7	65	17	17	
3.1.1.3.1.2	SP-CSC Proto. Design	50			50	33	65	83	83	
3.1.1.3.2	SP-CSC Proto. Construction	10	20		30	20	65	50	50	

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3.1.1.3.2.1	SP-CSC Proto. Constr. Manage.	10			10	7	65	17	17	
3.1.1.3.2.2	SP-CSC Proto. Components		12		12	8	65	20	20	
3.1.1.3.2.3	SP-CSC Proto. Boards		8		8	5	65	13	13	
3.1.1.3.3	SP-CSC Proto. Test	20			20	13	65	33	33	
3.1.1.3.4	SP-CSC ASIC and Board Design	50			50	33	65	83	83	
3.1.1.3.5	SP-CSC Active Components		34		34	22	65	56	56	
3.1.1.3.5.1	SP-CSC FPGAs		31		31	20	65	50	50	
3.1.1.3.5.2	SP-CSC FIFOs		1		1	1	65	2	2	
3.1.1.3.5.3	SP-CSC EPROMs		1		1	1	65	2	2	
3.1.1.3.5.4	SP-CSC Buffers		1		1	1	65	2	2	
3.1.1.3.6	SP-CSC Boards	20	11		31	20	65	51	51	
3.1.1.3.6.1	SP-CSC Constr. Manage	15			15	10	65	25	25	
3.1.1.3.6.2	SP-CSC Board Setup		2		2	1	65	3	3	
3.1.1.3.6.3	SP-CSC Board Fabrication		4		4	2	65	6	6	
3.1.1.3.6.4	SP-CSC Board Assembly		2		2	1	65	3	3	
3.1.1.3.6.5	SP-CSC Connectors-copper		1		1	0	65	1	1	
3.1.1.3.6.6	SP-CSC Misc components		1		1	1	65	2	2	
3.1.1.3.6.7	SP-CSC Front Panels and Hardware		2		2	1	65	3	3	
3.1.1.3.6.8	SP-CSC Inspection and test	5			5	3	65	8	8	
3.1.1.3.7	SP-CSC Installation	10			10	7	65	17	17	
3.1.1.3.8	SP-CSC Spares		11		11	7	65	18	18	
3.1.1.4	Overlap Sector Processors (SP-OVR)	170	76		246	160	65	406	406	
3.1.1.4.1	SP-OVR Design	60			60	39	65	99	99	
3.1.1.4.1.1	SP-OVR Initial System Design	10			10	7	65	17	17	
3.1.1.4.1.2	SP-OVR Proto. Design	50			50	33	65	83	83	
3.1.1.4.2	SP-OVR Proto. Construction	10	20		30	20	65	50	50	
3.1.1.4.2.1	SP-OVR Proto. Constr. Manage.	10			10	7	65	17	17	
3.1.1.4.2.2	SP-OVR Proto. Components		12		12	8	65	20	20	
3.1.1.4.2.3	SP-OVR Proto. Boards		8		8	5	65	13	13	
3.1.1.4.3	SP-OVR Proto. Test	20			20	13	65	33	33	
3.1.1.4.4	SP-OVR ASIC and Board Design	50			50	33	65	83	83	
3.1.1.4.5	SP-OVR Active Components		34		34	22	65	56	56	
3.1.1.4.5.1	SP-OVR FPGAs		31		31	20	65	50	50	
3.1.1.4.5.2	SP-OVR FIFOs		1		1	1	65	2	2	

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3.1.1.4.5.3	SP-OVR EPROMs		1		1	1	65	2	2	
3.1.1.4.5.4	SP-OVR Buffers		1		1	1	65	2	2	
3.1.1.4.6	SP-OVR Boards	20	11		31	20	65	51	51	
3.1.1.4.6.1	SP-OVR Constr. Manage	15			15	10	65	25	25	
3.1.1.4.6.2	SP-OVR Board Setup		2		2	1	65	3	3	
3.1.1.4.6.3	SP-OVR Board Fabrication		4		4	2	65	6	6	
3.1.1.4.6.4	SP-OVR Board Assembly		2		2	1	65	3	3	
3.1.1.4.6.5	SP-OVR Connectors-copper		1		1	0	65	1	1	
3.1.1.4.6.6	SP-OVR Misc components		1		1	1	65	2	2	
3.1.1.4.6.7	SP-OVR Front Panels and Hardware		2		2	1	65	3	3	
3.1.1.4.6.8	SP-OVR Inspection and test	5			5	3	65	8	8	
3.1.1.4.7	SP-OVR Installation	10			10	7	65	17	17	
3.1.1.4.8	SP-OVR Spares		11		11	7	65	18	18	
3.1.1.5	Clock&Control Cards (CCC)	78	36		114	46	40	159	159	
3.1.1.5.1	CCC Board Design	50			50	20	40	70	70	
3.1.1.5.2	CCC Active Components		21		21	8	40	29	29	
3.1.1.5.3	CCC Boards	18	8		26	10	40	36	36	
3.1.1.5.3.1	CCC Prod. Manage	15			15	6	40	21	21	
3.1.1.5.3.2	CCC Setup and tooling		2		2	1	40	3	3	
3.1.1.5.3.3	CCC Boards		2		2	1	40	3	3	
3.1.1.5.3.4	CCC Board assembly		1		1	0	40	2	2	
3.1.1.5.3.5	CCC Connectors-copper		0		0	0	40	0	0	
3.1.1.5.3.6	CCC Misc components		1		1	0	40	1	1	
3.1.1.5.3.7	CCC Front Panels and Hardware		1		1	0	40	2	2	
3.1.1.5.3.7	CCC Inspection and test	3			3	1	40	4	4	
3.1.1.5.4	CCC Installation	10			10	4	40	14	14	
3.1.1.5.5	CCC Spares		7		7	3	40	10	10	
3.1.1.6	Crate Monitor Cards		10		10	5	50	15	15	
3.1.1.7	Muon Backplanes	60	20		80	40	50	120	120	
3.1.1.7.1	Muon Backplane Design	60			60	30	50	90	90	
3.1.1.7.2	Muon Backplane Procure		20		20	10	50	30	30	
3.1.1.8	Crate Controllers		35		35	18	50	53	53	
3.1.1.9	Muon Crates		6		6	3	50	9	9	
3.1.1.10	Muon Power Supplies		23		23	12	50	35	35	

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3.1.1.11	Additional Cables		30		30	15	50	45	45	
3.1.1.11.1	Output Cables to Global Trigger		5		5	2	50	7	7	
3.1.1.11.2	Optical Fibers from MPC to SR		19		19	10	50	29	29	
3.1.1.11.3	Cables from SR-CSC to SR-OVR		6		6	3	50	9	9	
3.1.1.12	Trigger System Tests	43			43	21	50	64	64	
3.1.1.13	Trigger Project Management									
3.1.1.13.1	Tracking & Reporting									

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3.1.2	Calorimeter Regional Trigger	1,247	3,141		4,388	2,193	50	6,581	6,581	
3.1.2.1	Prototypes	301	140		441	202	46	643	643	
3.1.2.1.1	Proto. Receiver Card	89	18		106	34	32	140	140	
3.1.2.1.1.1	Design Proto. RC	63			63	20	32	84	84	
3.1.2.1.1.2	Order Proto. RC	9			9	3	32	12	12	
3.1.2.1.1.3	Purchase Proto. RC		18		18	6	32	23	23	
3.1.2.1.1.4	Test Proto RC	16			16	5	32	22	22	
3.1.2.1.2	Proto. Electron ID Card	65	7		72	36	50	108	108	
3.1.2.1.2.1	Design Proto. EIDC	44			44	22	50	65	65	
3.1.2.1.2.2	Order Proto. EIDC	3			3	2	50	5	5	
3.1.2.1.2.3	Purchase Proto. EIDC		7		7	4	50	11	11	
3.1.2.1.2.4	Test Proto EIDC	18			18	9	50	27	27	
3.1.2.1.3	Proto. Phase ASIC		50		50	25	50	75	75	
3.1.2.1.3.1	Phase ASIC Proto Des.						50			
3.1.2.1.3.2	Phase ASIC Proto. Order						50			
3.1.2.1.3.3	Phase ASIC Proto. Purchase		50		50	25	50	75	75	
3.1.2.1.4	Proto. BScan ASIC	14	50		64	32	50	96	96	
3.1.2.1.4.1	BScan ASIC Proto Des.	14			14	7	50	21	21	
3.1.2.1.4.2	BScan ASIC Proto. Order						50			
3.1.2.1.4.3	BScan ASIC Proto. Purchase		50		50	25	50	75	75	
3.1.2.1.5	Proto. Jet Summary Card	99	9		108	54	50	162	162	
3.1.2.1.5.1	Design Proto. JSC	69			69	34	50	103	103	
3.1.2.1.5.2	Order Proto. JSC	7			7	4	50	11	11	
3.1.2.1.5.3	Purchase Proto. JSC		9		9	5	50	14	14	
3.1.2.1.5.4	Test Proto JSC	23			23	11	50	34	34	
3.1.2.1.6	Proto. Clock & Control Card	35	6		41	21	50	62	62	
3.1.2.1.6.1	Design Proto. CCC	26			26	13	50	40	40	
3.1.2.1.6.2	Order Proto. CCC	7			7	4	50	11	11	
3.1.2.1.6.3	Purchase Proto. CCC		6		6	3	50	9	9	
3.1.2.1.6.4	Test Proto CCC	1			1	1	50	2	2	
3.1.2.1.7	Proto. Crate Monitor Card						-100			
3.1.2.2	Preproduction ASICs	243	310		553	258	47	811	811	
3.1.2.2.1	Electron ID ASIC	53	80		133	66	50	199	199	
3.1.2.2.1.1	EID ASIC Design	53			53	26	50	79	79	

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3.1.2.2.1.2	EID ASIC Order						50			
3.1.2.2.1.3	EID ASIC Purchase		80		80	40	50	120	120	
3.1.2.2.2	Adder ASIC	42	50		92	28	30	120	120	
3.1.2.2.2.1	Adder ASIC Design	42			42	13	30	55	55	
3.1.2.2.2.2	Adder ASIC Order						30			
3.1.2.2.2.3	Adder ASIC Purchase		50		50	15	30	65	65	
3.1.2.2.3	Sort ASIC	42	80		122	61	50	183	183	
3.1.2.2.3.1	Sort ASIC Design	42			42	21	50	63	63	
3.1.2.2.3.2	Sort ASIC Order						50			
3.1.2.2.3.3	Sort ASIC Purchase		80		80	40	50	120	120	
3.1.2.2.4	Phase ASIC	53	50		103	51	50	154	154	
3.1.2.2.4.1	Phase ASIC Design	53			53	26	50	79	79	
3.1.2.2.4.2	Phase ASIC Order						50			
3.1.2.2.4.3	Phase ASIC Purchase		50		50	25	50	75	75	
3.1.2.2.5	Boundary Scan ASIC	53	50		103	51	50	154	154	
3.1.2.2.5.1	Boundary Scan ASIC Design	53			53	26	50	79	79	
3.1.2.2.5.2	Boundary Scan ASIC Order						50			
3.1.2.2.5.3	Boundary Scan ASIC Purchase		50		50	25	50	75	75	
3.1.2.3	Test Facilities	18	60		78	39	50	117	117	
3.1.2.3.1	Design Test Facil.	13			13	6	50	19	19	
3.1.2.3.2	Procure Test Facil.		60		60	30	50	90	90	
3.1.2.3.3	Assemble Test Facil.	5			5	3	50	8	8	
3.1.2.4	Power Supplies	3	79		82	25	30	106	106	
3.1.2.4.1	Select Power Supplies	3			3	1	30	3	3	
3.1.2.4.2	Power Supply Procure		79		79	24	30	103	103	
3.1.2.4.2.1	PS Procure Manage						30			
3.1.2.4.2.2	Purchase Power Supplies		72		72	22	30	94	94	
3.1.2.4.2.3	2 PS Spares		7		7	2	30	9	9	
3.1.2.5	Crates	21	13		35	10	30	45	45	
3.1.2.5.1	Design Crate	18			18	5	30	23	23	
3.1.2.5.2	Procure Crates		13		13	4	30	17	17	
3.1.2.5.2.1	Crate Procure Manage						30			
3.1.2.5.2.2	Purchase Crates		12		12	4	30	16	16	
3.1.2.5.2.3	2 Crate Spares		1		1	0	30	2	2	

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3.1.2.5.3	Test Crates	4			4	1	30	5	5	
3.1.2.6	Backplane	64	130		194	105	54	299	299	
3.1.2.6.1	Design Backplane	57			57	31	54	88	88	
3.1.2.6.2	Backplane Procure	1	130		131	71	54	202	202	
3.1.2.6.2.1	Bkpl Procure Manage	1			1	1	54	2	2	
3.1.2.6.2.2	Bkpl Parts (Connectors)		92		92	50	54	142	142	
3.1.2.6.2.3	Bkpl Board		20		20	11	54	31	31	
3.1.2.6.2.4	Bkpl Assembly		6		6	3	54	9	9	
3.1.2.6.2.5	2 Bkpl Spares/Preprod		12		12	6	54	18	18	
3.1.2.6.3	Test Bkpl	5			5	3	54	8	8	
3.1.2.7	Clock & Control Card	67	65		132	53	40	185	185	
3.1.2.7.1	Design CCC	57			57	23	40	80	80	
3.1.2.7.2	CCC Procure	2	65		67	27	40	94	94	
3.1.2.7.2.1	CCC Procure Manage	2			2	1	40	3	3	
3.1.2.7.2.2	CCC Parts		39		39	16	40	55	55	
3.1.2.7.2.3	CCC Board		12		12	5	40	17	17	
3.1.2.7.2.4	CCC Assembly		8		8	3	40	11	11	
3.1.2.7.2.5	2 CCC Spares/Preprod		6		6	2	40	8	8	
3.1.2.7.3	Test CCC	8			8	3	40	11	11	
3.1.2.8	Receiver Card	109	1,561		1,670	902	54	2,571	2,571	
3.1.2.8.1	Design RC	63			63	34	54	98	98	
3.1.2.8.2	RC Procure	4	1,561		1,565	845	54	2,410	2,410	
3.1.2.8.2.1	RC Procure Manage	4			4	2	54	6	6	
3.1.2.8.2.2	RC Parts		1,187		1,187	641	54	1,828	1,828	
3.1.2.8.2.3	RC Board		128		128	69	54	197	197	
3.1.2.8.2.4	RC Assembly		104		104	56	54	160	160	
3.1.2.8.2.5	16 RC Spares/Preprod		142		142	77	54	219	219	
3.1.2.8.3	Test RC	41			41	22	54	64	64	
3.1.2.9	Electron Identification Card	95	649		744	372	50	1,116	1,116	
3.1.2.9.1	Design EIC	57			57	29	50	86	86	
3.1.2.9.2	EIC Procure	2	649		652	326	50	978	978	
3.1.2.9.2.1	EIC Procure Manage	2			2	1	50	4	4	
3.1.2.9.2.2	EIC Parts		478		478	239	50	718	718	
3.1.2.9.2.3	EIC Board		64		64	32	50	96	96	

WBS Number	Description	EDIA (k\$)	M&S (k\$)	Mfg Labor (k\$)	Base Cost (k\$)	Cont (k\$)	Cont (%)	Total Cost (k\$)	DOE Request (k\$)	NSF Request (k\$)
3.1.2.9.2.4	EIC Assembly		48		48	24	50	72	72	
3.1.2.9.2.5	16 EIC Spares/Preprod		59		59	30	50	89	89	
3.1.2.9.3	Test EIC	35			35	18	50	53	53	
3.1.2.10	Jet Summary Card	67	103		170	85	50	254	254	
3.1.2.10.1	Design JSC	57			57	29	50	86	86	
3.1.2.10.2	JSC Procure	2	103		105	52	50	157	157	
3.1.2.10.2.1	JSC Procure Manage	2			2	1	50	3	3	
3.1.2.10.2.2	JSC Parts		71		71	36	50	107	107	
3.1.2.10.2.3	JSC Board		12		12	6	50	18	18	
3.1.2.10.2.4	JSC Assembly		10		10	5	50	15	15	
3.1.2.10.2.5	2 JSC Spares/Preprod		9		9	5	50	14	14	
3.1.2.10.3	Test JSC	8			8	4	50	12	12	
3.1.2.11	Cables		7		7	2	30	9	9	
3.1.2.13	Crate Monitor Card						-100			
3.1.2.14	Trigger Tests	260	22		282	141	50	423	423	
3.1.2.14.1	Trigger Subsystem Tests	106			106	53	50	158	158	
3.1.2.14.2	Trigger System Installation	155	22		177	88	50	265	265	
3.1.2.14.2.1	Shipping		22		22	11	50	33	33	
3.1.2.14.2.2	Remote Site Commission	106			106	53	50	160	160	
3.1.2.14.2.3	Installation	48			48	24	50	72	72	
3.1.2.15	Trigger Project Management									
3.1.2.15.1	Tracking & Reporting									

WBS Number	Description	EDIA (k\$)	M&S (k\$)	Mfg Labor (k\$)	Base Cost (k\$)	Cont (k\$)	Cont (%)	Total Cost (k\$)	DOE Request (k\$)	NSF Request (k\$)
3.1.3	Physicist Activity									
3.1.3.1	Muon Trigger									
3.1.3.1.1	Simulation									
3.1.3.1.2	Software Development									
3.1.3.1.3	Testing									
3.1.3.1.4	Commissioning									
3.1.3.1.4	Management									
3.1.3.2	Calorimeter Trigger									
3.1.3.2.1	Simulation									
3.1.3.2.2	Software Development									
3.1.3.2.3	Testing									
3.1.3.2.4	Commissioning									
3.1.3.2.4	Management									