Figure 11.1 A typical computer design showing two multiwire buses, an internal bus connecting functional units within the CPU and an external bus for connecting additional computer subsystems. Often the subsystems on the external bus are on physically distinct circuit boards that plug into the bus as shown in Figure 11.2.
Figure 11.9  (a) The register set for the 8080 (shaded) and the Z80 8-bit microprocessors. (b) The register set for the 8086/8088 16-bit and 16/8-bit microprocessors. The 8080 register subset is shown shaded.