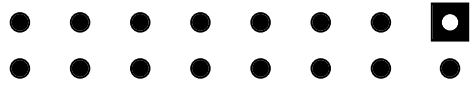


Indicate Completed Sections With Initials At Left Of Section Heading		
Date	Initials Used	Name

- ___ A. Record and Set Card Identification Information
- ___ 1. Record PCB number from assembled card.
 - ___ 2. Record barcode number from assembled card below.
 - ___ 3. Copy barcode ID to following pages of this checklist.
 - ___ 4. Refer to Reference table for SW8 settings.
 - ___ 5. Record SW8 value in table below.
 - ___ 6. Cut and/or install jumpers as needed to configure SW8.

1.	PCB I.D.	
2.	Barcode ID	
3.	Set SW 8 0 = open, 1 = jumper	

- ___ B. Protect Barcode
- ___ 1. Cover with Kapton Tape to protect from solvents.
 - ___ 2. Avoid taping over nearby vias.
 - ___ 3. Rub over Kapton with probe or pen body to seal Kapton to board and label.

- ___ C. Check Delay Values

Loc.	X	Y	Value	Condition	Status
U17	8.750	9.500	0 ns jumper		
U22	8.600	9.500	2005		
U23	8.450	9.500	2005		
U24	8.500	7.225	2005		
U69	9.947	9.207	1005		
U84	5.600	7.650	0 ns jumper		
U91	5.149	7.425	2005		
U111	4.225	10.350	1005		
U137	3.874	6.599	2005		
U140	9.349	9.207	2005		

___ D. Check fuse values

Loc.	Layer	X	Y	Value	Condition	Status
F1	top	10.05	14.2	1 amp		
F2	top	10.025	10.525	1 amp		
F3	top	9.925	0.675	4 amp		
F4	top	9.925	0.3	4 amp		

___ E. Axial capacitors

- ___ 1. Check polarity for axial capacitors installed. Pinched end = positive, black ring = negative.
- ___ 2. Install capacitor modules as needed.

Loc.	Layer	X	Y	Value	Action	Condition	Status
C4	top	9.775	10.175	220 uF, 10V	polarity/value		
C33	top	7.913	4.525	100 uF, 25V	polarity/value		
C58	top	7.009	5.438	100 uF, 25V	polarity/value		
C194	top	1.800	9.975	2V mini-module	install module "+" opposite location text		
C197	top	1.960	3.942	5V mini-module	install module "+" opposite location text		
C202	top	1.300	13.025	33uF, 100V	polarity/value		
C203	top	1.350	6.650	33uF, 100V	polarity/value		

___ F. Jumper locations

- ___ 1. Confirm or correct as required.

Loc	X	Y	Layer	Size	Test	Correction	Status
SW1	9.575	10.725	top	4x	1-8 jumped		
SW2	9.375	0.575	top	single	1-2 (full)		
SW3	8.575	10.675	top	4x	open		
SW4	7.925	10.675	top	4x	1-8 jumped		
SW5	7.9	13.65	top	8x	switch or empty, all open/OFF		
SW6	7.7	11.25	top	4x	1-8, 3-6 jumped		
SW7	7	9.6	top	4x	2-7 jumped		
SW8	6.575	14	top	8x	set in step A		
SW9	6.125	10.3	top	4x	1-8 jumped		
SW10	2.9	11.625	top	single	open		
SW11	2.9	11.325	top	single	open		
SW12	2.9	5.1	top	single	open		
SW13	2.9	4.8	top	single	open		

___ G. Check Sort ASICS

- ___ 1. Verify correct ASIC type and #1 pin rotation.
- ___ 2. Visually inspect for solder shorts and/or loose pins.
- ___ 3. Clean with alcohol and attach ASIC heatsinks.

Loc.	X	Y	Condition	Heatsink	Status
U8	9.175	8.175			
U9	9.175	6.250			
U10	9.175	3.575			
U11	9.175	1.700			

___ H. Check EISO ASICS

- ___ 1. Verify correct ASIC type and #1 pin rotation.
- ___ 2. Visually inspect for solder shorts and/or loose pins.
- ___ 3. Clean with alcohol and attach ASIC heatsinks.

Loc.	X	Y	Condition	Heatsink	Status
U129	2.000	8.375			
U130	2.000	1.975			

___ I. Install DCC1: Vicor V48B2C100AL

- ___ 1. Assemble converter, thermal pad, heatsink, and mounting kit.
- ___ 2. Wrap shield/converter joint with copper tape.
- ___ 3. Seat pin sockets fully on converter pins.
- ___ 4. Apply no-clean flux on top side of PCB.
- ___ 5. Install insulator, shield, converter, *and ground mod ring terminal* on PCB.
- ___ 6. Solder pin sockets and large pins to PCB.
- ___ 7. Test for shorts to shield/ground testpoint.

Sample Readings Taken On 4K ohm Scale

Sockets viewed from bottom layer		Sample readings		Test	Ass'y
		Board	Ass'y	Ass'y	Stat
●	+Vout	.000	.000		
●	+S	.000	.000		
●	SC	0L	0L		
●	-S	.021	.019		
●	-Vout	.021	.019		

Sockets viewed from bottom layer		Sample readings		Test	Ass'y
		Board	Ass'y	Ass'y	Stat
●	+Vin	0L	0L		
●	PC	0L	0L		
●	PR	0L	.525		
●	-Vin	.000	.000		

Special Condition for EISO DCC1	Condition	Status
Ring terminal added between mounting screw and -Vin pin socket, solder lug end to pin socket.		

- ___ J. Install DCC2: Vicor V48C5C50AL
- ___ 1. Assemble converter, thermal pad, heatsink, and mounting kit.
 - ___ 2. Wrap shield/converter joint with copper tape.
 - ___ 3. Seat pin sockets fully on converter pins.
 - ___ 4. Apply no-clean flux on top side of PCB.
 - ___ 5. Install insulator, shield, and converter on PCB.
 - ___ 6. Solder pin sockets and large pins to PCB.
 - ___ 7. Test for shorts to shield/ground testpoint.

Sample Readings Taken On 4K ohm Scale

Sockets viewed from bottom layer	Sample readings		Test	Ass'y
	Board	Ass'y	Ass'y	Stat
● +Vout	.000	.000		
● SC	0L	0L		
● -Vout	.092	.081		

Sockets viewed from bottom layer	Sample readings		Test	Ass'y
	Board	Ass'y	Ass'y	Stat
● +Vin	0L	0L		
● PC	0L	0L		
● PR	0L	.375		
● -Vin	.001	.000		

- ___ K. Check CTS resistors for shorts.
- ___ 1. Remove switch tape and set all switch positions to "OFF."
 - ___ 2. Visually inspect value printed on CTS resistor for correct rating.
 - ___ 3. Test neighboring pads for shorts or each resistor for correct value.

Comp	Layer	X	Y	BOM Part	Value	Shorts	Status
R41	top	8.375	13.875	753101102G			
R76	top	6.325	13.675	753101102G			

- ___ L. Connector hardware:
- ___ 1. Confirm hardware present and loctite evident for P1,J2.
 - ___ 2. Install replacements and/or apply loctite as needed.
 - ___ 2. 300 pin Amp connector must have 1 thread forming screw per module, 2 per module optimal.

Loc.	connector	hardware	loctite	Status
J2	300 pin Amp RA	2-56		
J2	300 pin Amp RA	#2 thread form	n/a	
P1	128 pin Erni RA	2-56		

- ___ M. General Visual Inspection Note problems below and in e-log.
- ___ 1. Check overall for incorrectly rotated parts.
- ___ 2. Check overall for missing parts.
- ___ 3. Check overall for solder bridges, bent pins, etc.
- ___ 4. Check fine pitch parts for solder bridges.
- ___ 5. Check overall for clear vias.

Location	Reqst (date/init)	Problem	Correction	Completed (date/comment)	By init.