

```
### Log file for this test: /afs/hep.wisc.edu/cms/RCTlog/daffodil/EIC_2004-07-29.log ### Location of log file
```

```
#####  
##### EIC Test 8 - Setting thresholds #####  
### Test run on 2004-07-29_17:50:22  
### HOST computer is: daffodil  
### Run in vmedia kumac: eiso_threshold.txt  
Please fill in the thrshold CHECKLIST.  
#####
```

```
Device to open: /dev/btp96  
Device to open: /dev/btp160 SBS successfully booted and talked to the cards in the crate  
Device to open: /dev/btp64
```

```
Enter command (help for usage)>
```

```
RCT boot succeeded with 9 cards. The CCC, 7 RCs and the EIC under test should be plugged in
```

```
Enter command (help for usage)> Enter command (help for usage)> Enter command (help for usage)>
```

```
Zero memories first.
```

```
Device to open: /dev/btp96
```

```
Device to open: /dev/btp160
```

```
Device to open: /dev/btp64
```

```
RCTCrate::initialize() : vmeReset() successful
```

```
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
```

```
RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000
```

```
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 15000000
```

```
rctCrateTest: initialize() succeeded
```

```
Cards in the crate are: 54ba
```

```
RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (f900, 16000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (f900, 16000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
```

```
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
```

```
RCTCrate::doZeroPatternTest() : Loading EIC (f800, 15000000)
```

```
RCTCrate::doZeroPatternTest() : Verifying EIC (f800, 15000000)
```

```
rctCrateTest: All tests successful
```

```
Now start vmedia script
```

```
*****  
*** this is vmedia script eiso_threshold.txt ***  
*****
```

```
for this test, the crate has to be loaded with all seven rc's Important
```

```
Device to open: /dev/btp96
```

```
Device to open: /dev/btp160
```

```
Device to open: /dev/btp64
```

```
12000006 -> F565
```

```
14000006 -> FA05
```

```
16000006 -> F960
18000006 -> F600
1B000006 -> F760
1D000006 -> FE00
1F000006 -> F820
12000000 -> 0202
14000000 -> 0202
16000000 -> 0202
18000000 -> 0202
1B000000 -> 0202
1D000000 -> 0202
1F000000 -> 0202
```

**Should  
see  
these  
values  
in  
the  
registers**

for this test, the eic has to be in slot 2 **Important**

eic has barcode:

```
15000002 -> F809 Compare this number with the EIC bar code
```

```
Continue <return> ? Exit <Ctrl-D> ? type <return> here
```

initial setup done

start with r0:

```
threshold 0, expect to see one non-iso with et=f -- next
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 1, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold still 1, expect to see non-iso with et=f and iso with et=2 -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 2, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold still2, expect to see non-iso with et=f and iso with et=3 -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 3, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

start with r1:

```
threshold 0, expect to see one non-iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 1, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold still 1, expect to see non-iso with et=f and iso with et=2 -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 2, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold still2, expect to see non-iso with et=f and iso with et=3 -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

```
threshold 3, expect to see non-iso with et=a and iso with et=f -- next ?
```

```
Continue <return> ? Exit <Ctrl-D> ?
```

all done

VMEDia>

```
VMEDia> exit type 'exit' here
```

Bye

```
##### EIC Test 8 End #####
```

**Repeat:**

**Check signal following checklist -  
when done type <return> for next signal**