

### Log file for this test: /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC\_2004-08-26

.log ### **Location of log file**

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##### RC Test 3 - Write/read data to/from LUTs #####  
### Test run on 2004-08-26\_16:28:00  
### HOST computer is: daffodil  
### Run executable rctCrateTest  
#####

Device to open: /dev/btp96 **SBS successfully**  
Device to open: /dev/btp160 **booted and talked**  
Device to open: /dev/btp64 **to the cards in the crate**

Enter command (help for usage)>

**RCT boot succeeded with 2 cards.** **The CCC and the RC to be tested should be plugged in**

Enter command (help for usage)>

Device to open: /dev/btp96

Device to open: /dev/btp160

Device to open: /dev/btp64

RCTCrate::initialize() : **vmeReset() successful**

RCTCrate::initialize() : Defined RCTClockControlCard 10000000

RCTCrate::initialize() : Defined RCTReceiverCard with **address 18000000**

rctCrateTest: **initialize() succeeded**

**Address corresponds  
to RC being in slot 3**

Cards in the crate are: 80

RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)

RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)

RCTCrate::doIncreasingPatternTest() : Loading RC (f600, 18000000)

RCTCrate::doIncreasingPatternTest() : Verifying RC (f600, 18000000)

RCTCrate::doDecreasingPatternTest() : Loading RC (f600, 18000000)

RCTCrate::doDecreasingPatternTest() : Verifying RC (f600, 18000000)

RCTCrate::doRandomPatternTest() : Loading RC (f600, 18000000)

RCTCrate::doRandomPatternTest() : Verifying RC (**f600**, 18000000)

rctCrateTest: **All tests successful**

**Compare this number to RC bar code**

##### RC Test 3 End #####