```
### Log file for this test:  /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26
.log  ###
```

Location of log file

```
##################################################################
################ RC Test 6c - Data sharing via cables ##############
### Test run on  2004-08-26_18:50:37
### HOST computer is:  daffodil
### Run in vmedia kumac: check_j3.txt
 Please fill in the data sharing CHECKLIST.
##################################################################

********
!!!! RC to be tested in slot 1 !!!!
```

IMPORTANT !

```
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
Enter command (help for usage)>
RCT boot succeeded with 9 cards.
Enter command (help for usage)> Enter command (help for usage)> Enter command (h
elp for usage)>

Zero memories first.
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
RCTCrate::initialize() : vmeReset() successful
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 13000000
rctCrateTest: initialize() succeeded
Cards in the crate are: 54ae
RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Loading RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Verifying RC (0, 16000000)
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Loading EIC (f900, 13000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (f900, 13000000)
rctCrateTest: All tests successful
Now start vmedia script check_j3.txt

**************************************************
*** This is vmedia script check_j3.txt **************
**************************************************
for this test, the rc to be tested has to be either in slot 1 or in slot 0
rc in slot 1 has barcode
Device to open: /dev/btp96
Device to open: /dev/btp160
```

SBS successfully booted and talked to the cards in the crate

The CCC, 1 EIC in slot 1, 6 RCs and the RC to be tested in slot 1 should be plugged in

Check that the verification doesn't fail. When it does, leave the script (Ctrl-D) and vmedia (exit) and redo >run_RC_test6c

Important

```
Device to open: /dev/btp64
14000006 -> FA05
rc in slot 0 has barcode
12000006 -> F565 Compare this number with the RC bar code
the eic has to be in slot 1  Important
Continue <return> ? Exit <Ctrl-D> ? type <return> here
for this test, the crate has to be loaded with all seven rc's
12000006 -> F565
14000006 -> FA05
16000006 -> 0060
18000006 -> F600
1B000006 -> F760   These
1D000006 -> FE00   values
1F000006 -> F820   should
12000000 -> 0202   be
14000000 -> 0202   read
16000000 -> 0202   back
18000000 -> 0202
1B000000 -> 0202
1D000000 -> 0202
1F000000 -> 0202
12000006 -> F565
14000006 -> FA05
plug in cable in j3 from card 0 to card 1. Follow these directions, no need to first power down
sw corner - done   Should see a "1" on U66, pins 24, 28, 4, 6
VMEDia>
VMEDia>
VMEDia>  exit type 'exit' here
Bye


################ RC Test 6c End ###################################
```