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### Log file for this test: /afs/hep.wisc.edu/cms/RCTlog/daffodil/RC_2004-08-26
.log ### Location of log file

#####
##### RC Test 8 - Pattern tests (electrons) #####
### Test run on 2004-08-26_19:14:30
### HOST computer is: daffodil
### Run executable output_electron
Please, check bit patterns on JSC Mezz J5, J6.
#####

Device to open: /dev/btp96
Device to open: /dev/btp160 SBS successfully booted and talked
Device to open: /dev/btp64 to the cards in the crate
Enter command (help for usage)> Crate has to be fully loaded, with RC under test in slot 3:
RCT boot succeeded with 16 cards. 1 CCC, 1 JSC, 7 EICs, 6 RC and the RC under test
Enter command (help for usage)> Enter command (help for usage)> Enter command (h
elp for usage)>
Zero memories first.
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
RCTCrate::initialize() : vmeReset() successful
RCTCrate::initialize() : Defined RCTClockControlCard 10000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 12000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 14000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 16000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 18000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1b000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1d000000
RCTCrate::initialize() : Defined RCTReceiverCard with address 1f000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 11000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 13000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 15000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 17000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 1a000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 1c000000
RCTCrate::initialize() : Defined RCTElectronIsolationCard with address 1e000000
RCTCrate::initialize() : Defined RCTJetSummaryCard with address 19000000
rctCrateTest: initialize() succeeded
Cards in the crate are: 7fff
RCTCrate::doZeroPatternTest() : Loading RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f500, 12000000)
RCTCrate::doZeroPatternTest() : Loading RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fa00, 14000000)
RCTCrate::doZeroPatternTest() : Loading RC (f900, 16000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f900, 16000000)
RCTCrate::doZeroPatternTest() : Loading RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f600, 18000000)
RCTCrate::doZeroPatternTest() : Loading RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f700, 1b000000)
RCTCrate::doZeroPatternTest() : Loading RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Verifying RC (fe00, 1d000000)
RCTCrate::doZeroPatternTest() : Loading RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Verifying RC (f800, 1f000000)
RCTCrate::doZeroPatternTest() : Loading EIC (0, 11000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 11000000)
RCTCrate::doZeroPatternTest() : Loading EIC (0, 13000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 13000000)
RCTCrate::doZeroPatternTest() : Loading EIC (f800, 15000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (f800, 15000000)
RCTCrate::doZeroPatternTest() : Loading EIC (0, 17000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 17000000)

```

Check that the verification doesn't fail. When it does, leave the script (Ctrl-D) and vmedia (exit) and redo >run_EIC_test7

```
RCTCrate::doZeroPatternTest() : Loading EIC (0, 1a000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 1a000000)
RCTCrate::doZeroPatternTest() : Loading EIC (0, 1c000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (0, 1c000000)
RCTCrate::doZeroPatternTest() : Loading EIC (f900, 1e000000)
RCTCrate::doZeroPatternTest() : Verifying EIC (f900, 1e000000)
RCTCrate::doZeroPatternTest() : Loading JSC (ff00, 19000000)
RCTCrate::doZeroPatternTest() : Verifying JSC (ff00, 19000000)
```

rctCrateTest: All tests successful

Now fill the appropriate patterns into the memory.

```
Device to open: /dev/btp96
Device to open: /dev/btp160
Device to open: /dev/btp64
```

```
Card E0 is inserted
Card R0 is inserted
Card E1 is inserted
Card R1 is inserted
Card E2 is inserted
Card R2 is inserted
Card E3 is inserted
Card R3 is inserted
Card JS is inserted
Card E4 is inserted
Card R4 is inserted
Card E5 is inserted
Card R5 is inserted
Card E6 is inserted
Card R6 is inserted
```

A total of 15 cards inserted

```
Incrementing 0 memory for 11400000
Incrementing 0 memory for 13400000
Incrementing 0 memory for 15400000
Incrementing 0 memory for 17400000
Incrementing 0 memory for 1A400000
Incrementing 0 memory for 1C400000
Incrementing 0 memory for 1E400000
```

```
Programming Card 0
Programming Card 1
Programming Card 2
Programming Card 3
Programming Card 4
Programming Card 5
Programming Card 6
```

You can now start checking the output pattern on JSC Mezz J5, J6.

RC Test 8 End