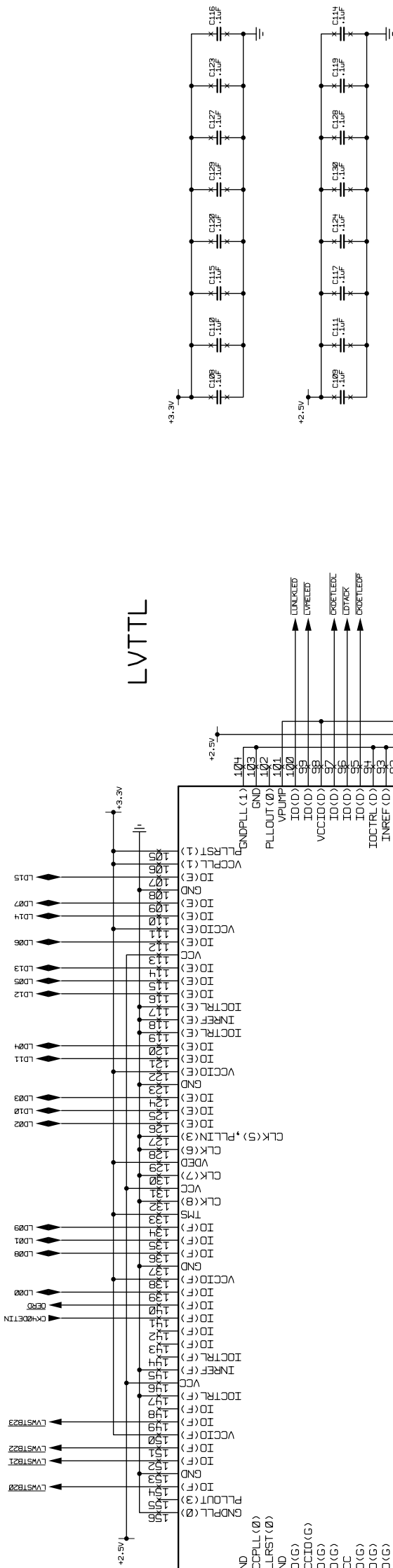


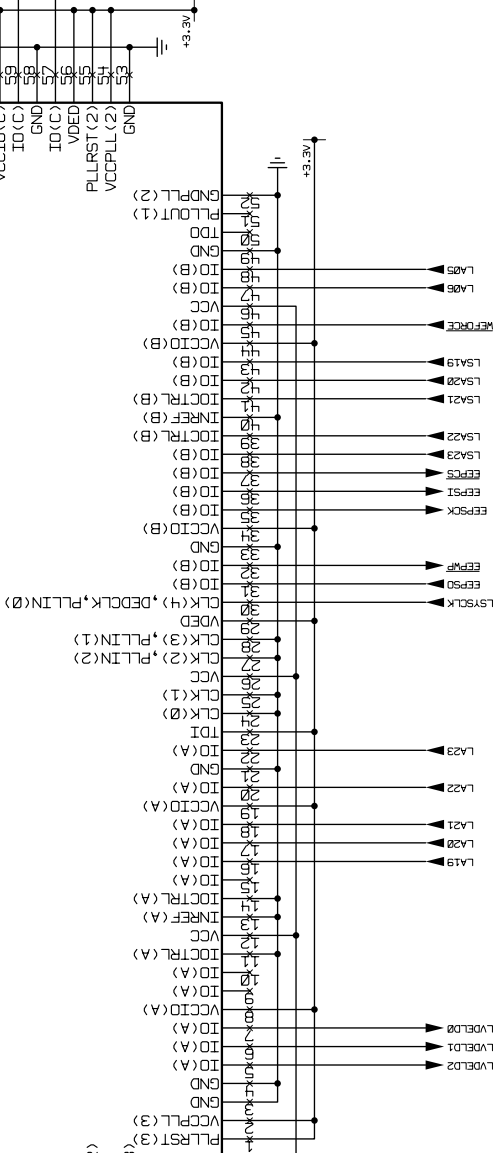
LVTTL



U69

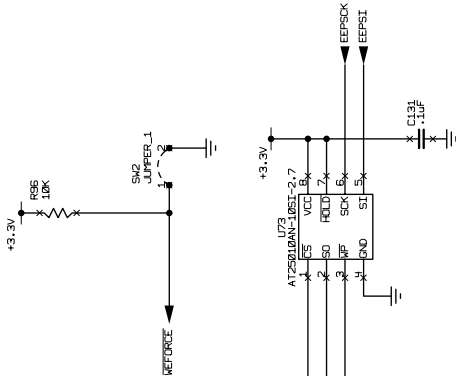
QL6325E-7PQ208C

LVTTL



LVTTL

LVTTL



Changes	By	Date	Drawn	TAG	Project	CMS CLOCK FANOUT CARD (CRATE)
Rev B	TAG	030CT06	Date 28JUN05	CHK'D	CONTROL FPGA	
			Scale		BLOCK: CIC_TOP	SHEET: 7 OF 8
					High Energy Physics	
					University of Wisconsin	