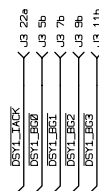
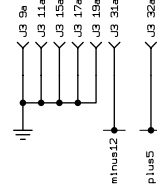
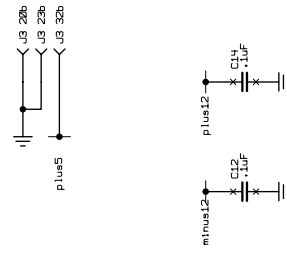
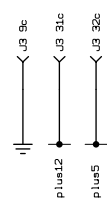
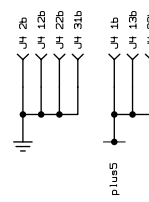
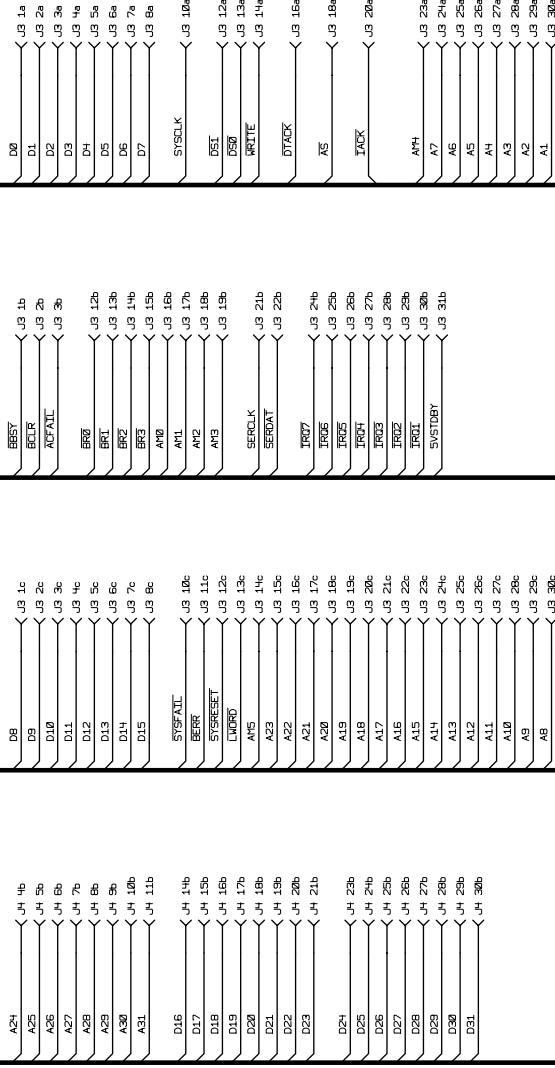
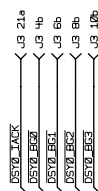
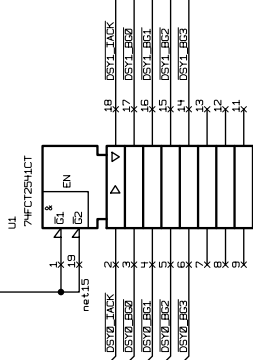
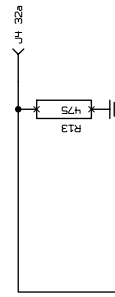


VME

DSY_OUT

DSY_IN



Changes		By	Date	Drawn	JLL	Project	CMS CAL TRIGGER PROCESSOR
				Date	2/5/96	BACKPLANE - SPARE SLOT	
				CHK'D		BLOCK: SPARE	
				Scale		High Energy Physics	
REV A		JLL	10/28/99	C0521		University of Wisconsin	