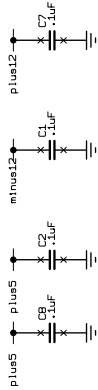
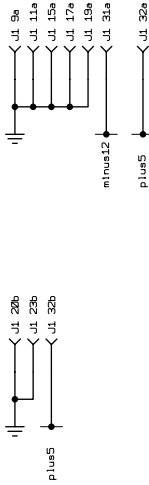
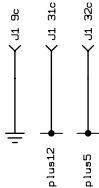
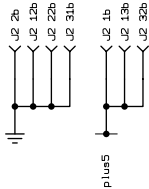
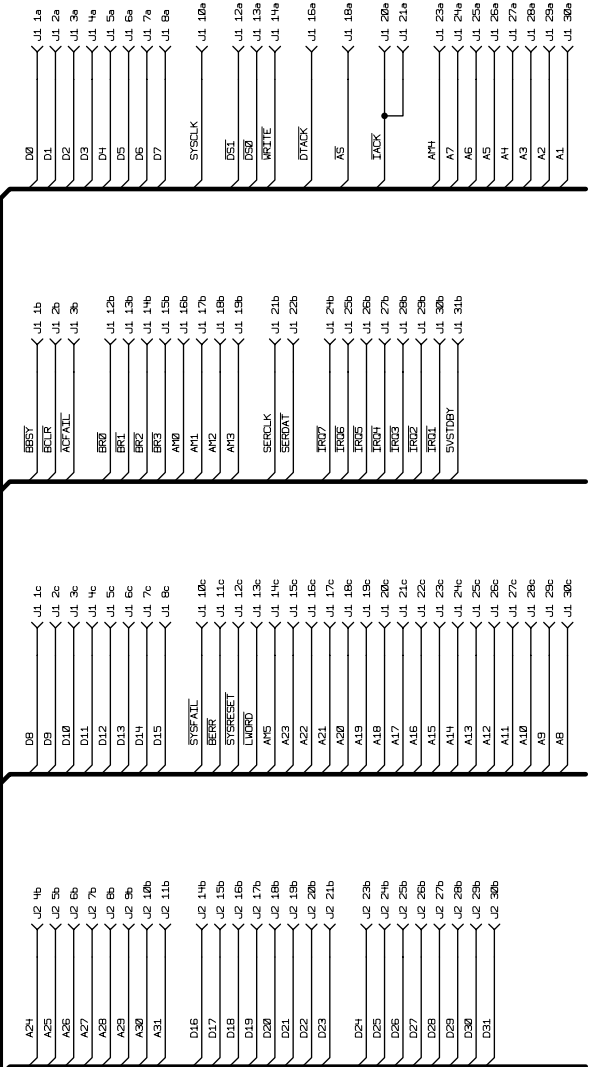
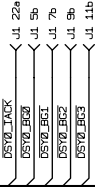


VME

DSY_OUT



Changes	By	Date	Drawn	JLL	Project	CMS CAL TRIGGER PROCESSOR
			Date	2/5/96	BACKPLANE - VME CONTROL SLOT	
			CHK'D		BLOCK: VME_CNTRL	
			Scale			
					High Energy Physics	
REV A	JLL	10/27/99		C0540	University of Wisconsin	