VME

DSY_OUT

DSY_IN

J5 2d
J5 12d
J5 22d
J5 31d
plus5 — J5 1d, J5 13d, J5 32d

plus12 — J5 9c, J5 31c
plus5 — J5 32c

plus5 — J5 20b, J5 23b, J5 32b

minus12 — J5 9a, J5 11a, J5 15a, J5 17a, J5 19a, J5 31a
plus5 — J5 32a

J5 3d
R14 47Ω

U2
74FCT2541CT
1G1, 2G2, EN
net166

DSY1_IACK	2	18	DSY2_IACK
DSY1_BG0	3	17	DSY2_BG0
DSY1_BG1	4	16	DSY2_BG1
DSY1_BG2	5	15	DSY2_BG2
DSY1_BG3	6	14	DSY2_BG3

DSY1_IACK — J5 21a
DSY1_BG0 — J5 4b
DSY1_BG1 — J5 6b
DSY1_BG2 — J5 8b
DSY1_BG3 — J5 10b

DSY2_IACK — J5 22a
DSY2_BG0 — J5 5b
DSY2_BG1 — J5 7b
DSY2_BG2 — J5 9b
DSY2_BG3 — J5 11b

A24 — J5 4d
A25 — J5 5d
A26 — J5 6d
A27 — J5 7d
A28 — J5 8d
A29 — J5 9d
A30 — J5 10d
A31 — J5 11d

D16 — J5 14d
D17 — J5 15d
D18 — J5 16d
D19 — J5 17d
D20 — J5 18d
D21 — J5 19d
D22 — J5 20d
D23 — J5 21d

D24 — J5 23d
D25 — J5 24d
D26 — J5 25d
D27 — J5 26d
D28 — J5 27d
D29 — J5 28d
D30 — J5 29d
D31 — J5 30d

D8 — J5 1c
D9 — J5 2c
D10 — J5 3c
D11 — J5 4c
D12 — J5 5c
D13 — J5 6c
D14 — J5 7c
D15 — J5 8c

SYSFAIL — J5 10c
BERR — J5 11c
SYSRESET — J5 12c
LWORD — J5 13c
AM5 — J5 14c
A23 — J5 15c
A22 — J5 16c
A21 — J5 17c
A20 — J5 18c
A19 — J5 19c
A18 — J5 20c
A17 — J5 21c
A16 — J5 22c
A15 — J5 23c
A14 — J5 24c
A13 — J5 25c
A12 — J5 26c
A11 — J5 27c
A10 — J5 28c
A9 — J5 29c
A8 — J5 30c

BBSY — J5 1b
BCLR — J5 2b
ACFAIL — J5 3b

BR0 — J5 12b
BR1 — J5 13b
BR2 — J5 14b
BR3 — J5 15b
AM0 — J5 16b
AM1 — J5 17b
AM2 — J5 18b
AM3 — J5 19b

SERCLK — J5 21b
SERDAT — J5 22b

IRQ7 — J5 24b
IRQ6 — J5 25b
IRQ5 — J5 26b
IRQ4 — J5 27b
IRQ3 — J5 28b
IRQ2 — J5 29b
IRQ1 — J5 30b
SYSTDBY — J5 31b

D0 — J5 1a
D1 — J5 2a
D2 — J5 3a
D3 — J5 4a
D4 — J5 5a
D5 — J5 6a
D6 — J5 7a
D7 — J5 8a

SYSCLK — J5 10a

DS1 — J5 12a
DS0 — J5 13a
WRITE — J5 14a

DTACK — J5 16a

AS — J5 18a

IACK — J5 20a

AM4 — J5 23a
A7 — J5 24a
A6 — J5 25a
A5 — J5 26a
A4 — J5 27a
A3 — J5 28a
A2 — J5 29a
A1 — J5 30a

plus5 — C16 .1uF
plus5 — C19 .1uF
minus12 — C15 .1uF
plus12 — C20 .1uF

CLK_CNTRL0

CLK_CNTRL1

CLK_CNTRL2

CLK_CNTRL3

CLK_CNTRL4

CLK_CNTRL5

CLK_CNTRL6

CLK_CNTRL7

CLK_CNTRL8

plus4B — C17 .047uF
plus4B — C21 .047uF
plus4B — C18 .047uF
plus4B — C22 .047uF

STAT8A_STAT — P5 a1
STAT8A_STAT — P5 b1
STAT8A_ACPT — P5 c1
STAT8A_ACPT — P5 d1
STAT8A_BX0 — P5 a2
STAT8A_BX0 — P5 b2
STAT8A_RST — P5 c2
STAT8A_RST — P5 d2
STAT8A_CLK — P5 a3
STAT8A_CLK — P5 b3
STAT8A_SLTID — P5 d3

STAT7B_STAT — P5 a13
STAT7B_STAT — P5 b13
STAT7B_ACPT — P5 c13
STAT7B_ACPT — P5 d13
STAT7B_BX0 — P5 a14
STAT7B_BX0 — P5 b14
STAT7B_RST — P5 c14
STAT7B_RST — P5 d14
STAT7B_CLK — P5 a15
STAT7B_CLK — P5 b15
STAT7B_SLTID — P5 d15

STAT7A_STAT — P5 a16
STAT7A_STAT — P5 b16
STAT7A_ACPT — P5 c16
STAT7A_ACPT — P5 d16
STAT7A_BX0 — P5 a17
STAT7A_BX0 — P5 b17
STAT7A_RST — P5 c17
STAT7A_RST — P5 d17
STAT7A_CLK — P5 a18
STAT7A_CLK — P5 b18
STAT7A_SLTID — P5 d18

STAT6B_STAT — P5 a28
STAT6B_STAT — P5 b28
STAT6B_ACPT — P5 c28
STAT6B_ACPT — P5 d28
STAT6B_BX0 — P5 a29
STAT6B_BX0 — P5 b29
STAT6B_RST — P5 c29
STAT6B_RST — P5 d29
STAT6B_CLK — P5 a30
STAT6B_CLK — P5 b30
STAT6B_SLTID — P5 d30

STAT6A_STAT — P5 a31
STAT6A_STAT — P5 b31
STAT6A_ACPT — P5 c31
STAT6A_ACPT — P5 d31
STAT6A_BX0 — P5 a32
STAT6A_BX0 — P5 b32
STAT6A_RST — P5 c32
STAT6A_RST — P5 d32
STAT6A_CLK — P5 a33
STAT6A_CLK — P5 b33
STAT6A_SLTID — P5 d33

STAT5B_STAT — P5 a40
STAT5B_STAT — P5 b40
STAT5B_ACPT — P5 c40
STAT5B_ACPT — P5 d40
STAT5B_BX0 — P5 a41
STAT5B_BX0 — P5 b41
STAT5B_RST — P5 c41
STAT5B_RST — P5 d41
STAT5B_CLK — P5 a42
STAT5B_CLK — P5 b42
STAT5B_SLTID — P5 d42

STAT5A_STAT — P5 a43
STAT5A_STAT — P5 b43
STAT5A_ACPT — P5 c43
STAT5A_ACPT — P5 d43
STAT5A_BX0 — P5 a44
STAT5A_BX0 — P5 b44
STAT5A_RST — P5 c44
STAT5A_RST — P5 d44
STAT5A_CLK — P5 a45
STAT5A_CLK — P5 b45
STAT5A_SLTID — P5 d45

STAT4A_STAT — P5 a19
STAT4A_STAT — P5 b19
STAT4A_ACPT — P5 c19
STAT4A_ACPT — P5 d19
STAT4A_BX0 — P5 a20
STAT4A_BX0 — P5 b20
STAT4A_RST — P5 c20
STAT4A_RST — P5 d20
STAT4A_CLK — P5 a21
STAT4A_CLK — P5 b21
STAT4A_SLTID — P5 d21

STAT3A_STAT — P5 a4
STAT3A_STAT — P5 b4
STAT3A_ACPT — P5 c4
STAT3A_ACPT — P5 d4
STAT3A_BX0 — P5 a5
STAT3A_BX0 — P5 b5
STAT3A_RST — P5 c5
STAT3A_RST — P5 d5
STAT3A_CLK — P5 a6
STAT3A_CLK — P5 b6
STAT3A_SLTID — P5 d6

STAT2B_STAT — P5 a7
STAT2B_STAT — P5 b7
STAT2B_ACPT — P5 c7
STAT2B_ACPT — P5 d7
STAT2B_BX0 — P5 a8
STAT2B_BX0 — P5 b8
STAT2B_RST — P5 c8
STAT2B_RST — P5 d8
STAT2B_CLK — P5 a9
STAT2B_CLK — P5 b9
STAT2B_SLTID — P5 d9

STAT2A_STAT — P5 a10
STAT2A_STAT — P5 b10
STAT2A_ACPT — P5 c10
STAT2A_ACPT — P5 d10
STAT2A_BX0 — P5 a11
STAT2A_BX0 — P5 b11
STAT2A_RST — P5 c11
STAT2A_RST — P5 d11
STAT2A_CLK — P5 a12
STAT2A_CLK — P5 b12
STAT2A_SLTID — P5 d12

STAT1B_STAT — P5 a22
STAT1B_STAT — P5 b22
STAT1B_ACPT — P5 c22
STAT1B_ACPT — P5 d22
STAT1B_BX0 — P5 a23
STAT1B_BX0 — P5 b23
STAT1B_RST — P5 c23
STAT1B_RST — P5 d23
STAT1B_CLK — P5 a24
STAT1B_CLK — P5 b24
STAT1B_SLTID — P5 d24

STAT1A_STAT — P5 a25
STAT1A_STAT — P5 b25
STAT1A_ACPT — P5 c25
STAT1A_ACPT — P5 d25
STAT1A_BX0 — P5 a26
STAT1A_BX0 — P5 b26
STAT1A_RST — P5 c26
STAT1A_RST — P5 d26
STAT1A_CLK — P5 a27
STAT1A_CLK — P5 b27
STAT1A_SLTID — P5 d27

STAT0B_STAT — P5 a34
STAT0B_STAT — P5 b34
STAT0B_ACPT — P5 c34
STAT0B_ACPT — P5 d34
STAT0B_BX0 — P5 a35
STAT0B_BX0 — P5 b35
STAT0B_RST — P5 c35
STAT0B_RST — P5 d35
STAT0B_CLK — P5 a36
STAT0B_CLK — P5 b36
STAT0B_SLTID — P5 c36

STAT0A_STAT — P5 a37
STAT0A_STAT — P5 b37
STAT0A_ACPT — P5 c37
STAT0A_ACPT — P5 d37
STAT0A_BX0 — P5 a38
STAT0A_BX0 — P5 b38
STAT0A_RST — P5 c38
STAT0A_RST — P5 d38
STAT0A_CLK — P5 a39
STAT0A_CLK — P5 b39
STAT0A_SLTID — P5 d39

Changes	By	Date	Drawn JLL	Project CMS CAL TRIGGER PROCESSOR
			Date 2/5/96	BACKPLANE - CLOCK DISTRIBUTION
			CHK'D	BLOCK: CLK_DIST
			Scale	High Energy Physics
REV A	JLL	29/10/99	D0522	University of Wisconsin